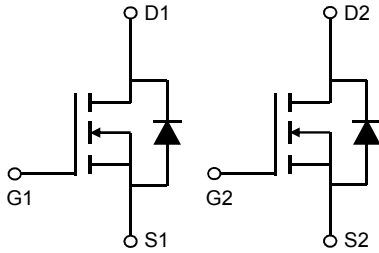
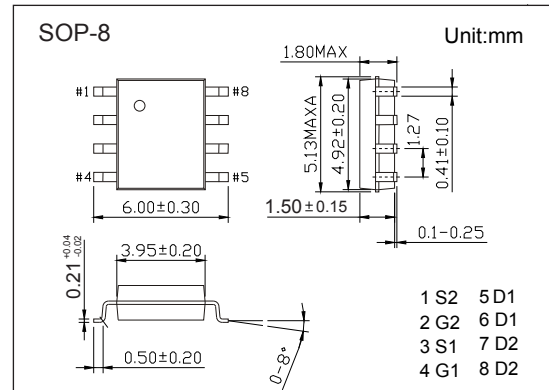


## Dual N-Channel MOSFET

### AO4892 (K04892)

#### ■ Features

- $V_{DS} (V) = 100V$
- $I_D = 4A (V_{GS} = 10V)$
- $R_{DS(ON)} < 68m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 94m\Omega (V_{GS} = 4.5V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{DS}$	100	V	
Gate-Source Voltage	$V_{GS}$	$\pm 20$		
Continuous Drain Current	$I_D$	$T_A=25^\circ C$	4	A
		$T_A=70^\circ C$	3	
Pulsed Drain Current	$I_{DM}$	25		
Avalanche Current	$I_{AS}$	4		
Avalanche Energy	$L=0.1mH$	$E_{AS}$	0.8	mJ
Power Dissipation	$P_D$	$T_A=25^\circ C$	2	W
		$T_A=70^\circ C$	1.3	
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	$t \leq 10s$	62.5	$^\circ C/W$
		Steady-State	90	
Thermal Resistance.Junction- to-Lead	$R_{thJL}$	40		
Junction Temperature	$T_J$	150	$^\circ C$	
Storage Temperature Range	$T_{stg}$	-55 to 150		

## Dual N-Channel MOSFET

### AO4892 (K04892)

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	100			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V			1	μA
		V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.7		2.8	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =4A			68	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =4A T <sub>J</sub> =125°C			126	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A			94	
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5A	25			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =4A		12.5		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =50V, f=1MHz		415		pF
Output Capacitance	C <sub>oss</sub>			32		
Reverse Transfer Capacitance	C <sub>rss</sub>			3		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.7		2.1	Ω
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =4A		6.5	12	nC
Total Gate Charge (4.5V)				3	6	
Gate Source Charge	Q <sub>gs</sub>			1.5		
Gate Drain Charge	Q <sub>gd</sub>			1.5		
Turn-On DelayTime	t <sub>d(on)</sub>			4		
Turn-On Rise Time	t <sub>r</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, R <sub>L</sub> =12.5Ω, R <sub>GEN</sub> =3Ω		2		
Turn-Off DelayTime	t <sub>d(off)</sub>			15		
Turn-Off Fall Time	t <sub>f</sub>			2		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 4A, di/dt= 500A/us		16		nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			44		
Maximum Body-Diode Continuous Current	I <sub>S</sub>				2.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V			1	V

Note.The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

#### ■ Marking

Marking	4892 KA****
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## Dual N-Channel MOSFET AO4892 (KO4892)

### Typical Characteristics

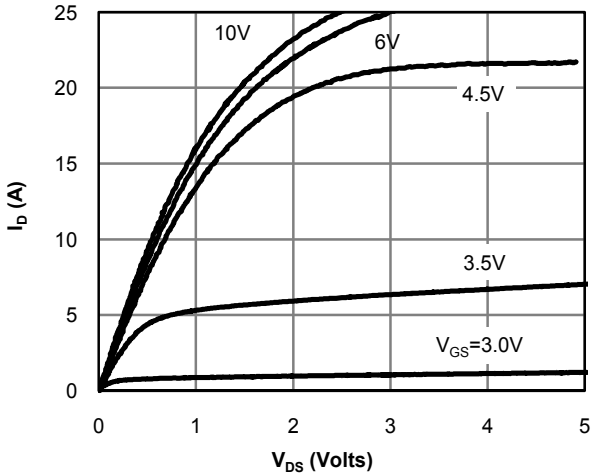


Fig 1: On-Region Characteristics (Note E)

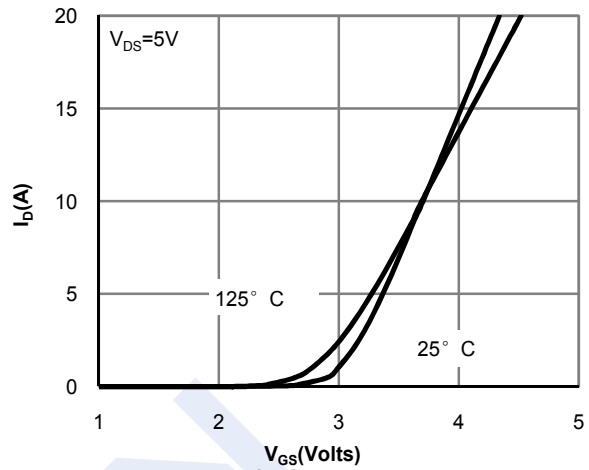


Figure 2: Transfer Characteristics (Note E)

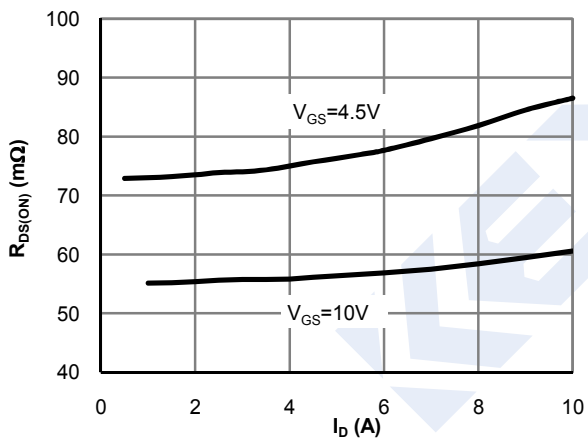


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

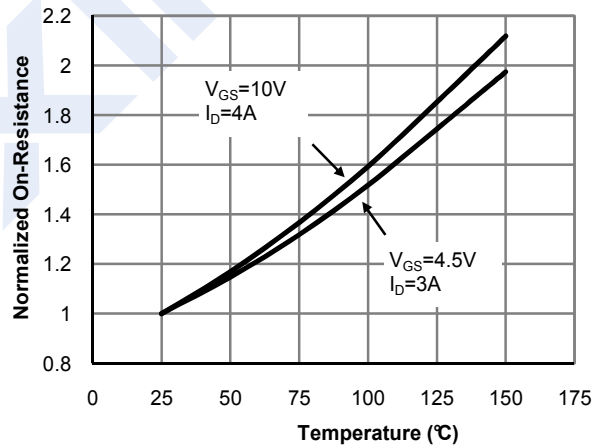


Figure 4: On-Resistance vs. Junction Temperature

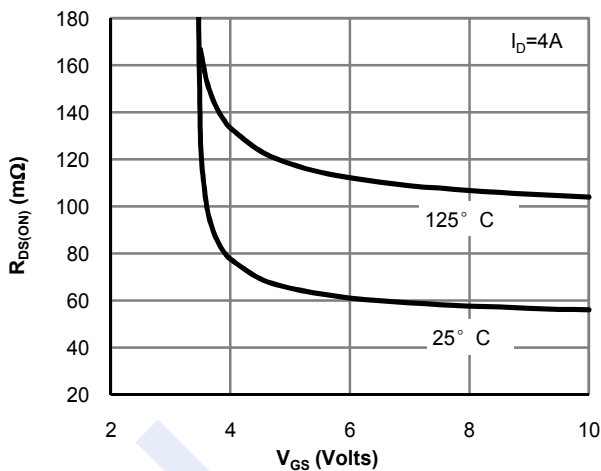


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

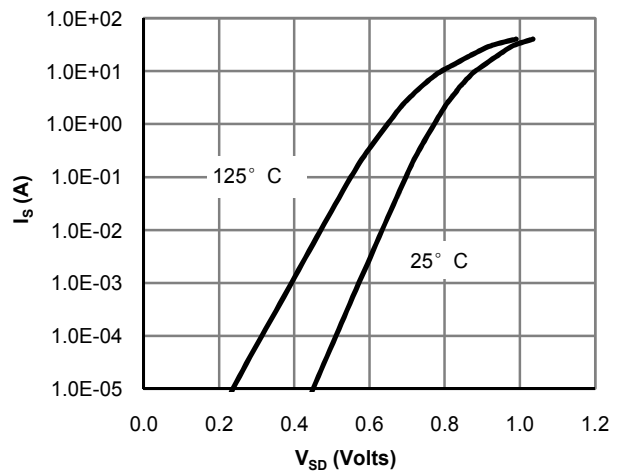


Figure 6: Body-Diode Characteristics (Note E)

## Dual N-Channel MOSFET AO4892 (KO4892)

■ Typical Characteristics

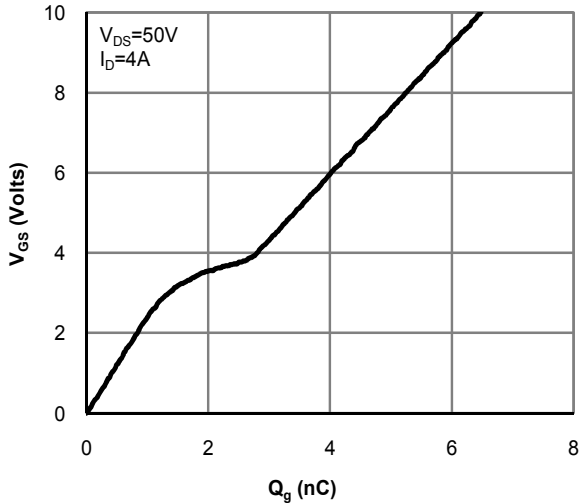


Figure 7: Gate-Charge Characteristics

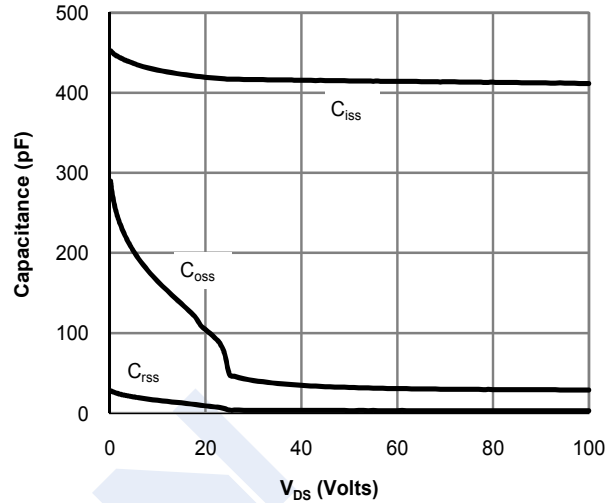


Figure 8: Capacitance Characteristics

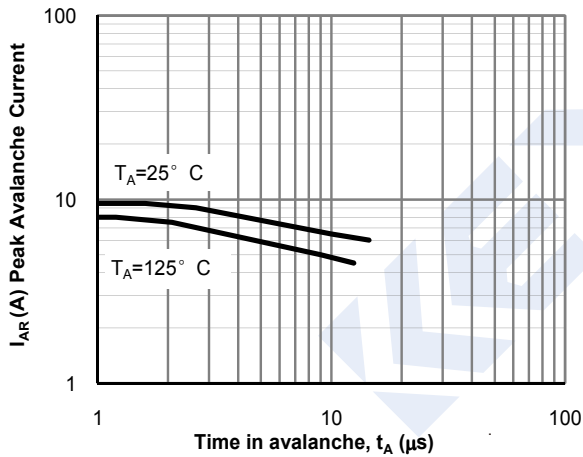


Figure 12: Single Pulse Avalanche capability

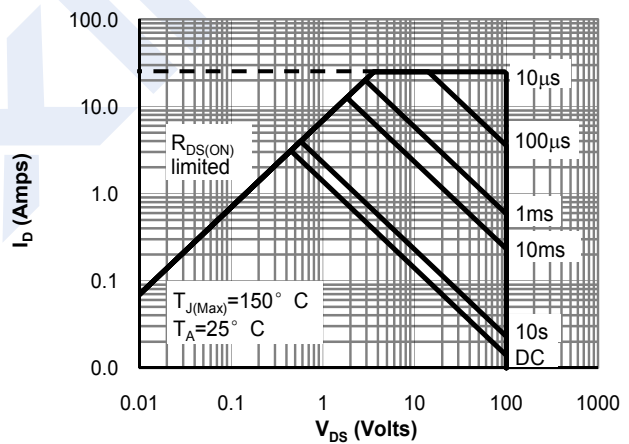


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

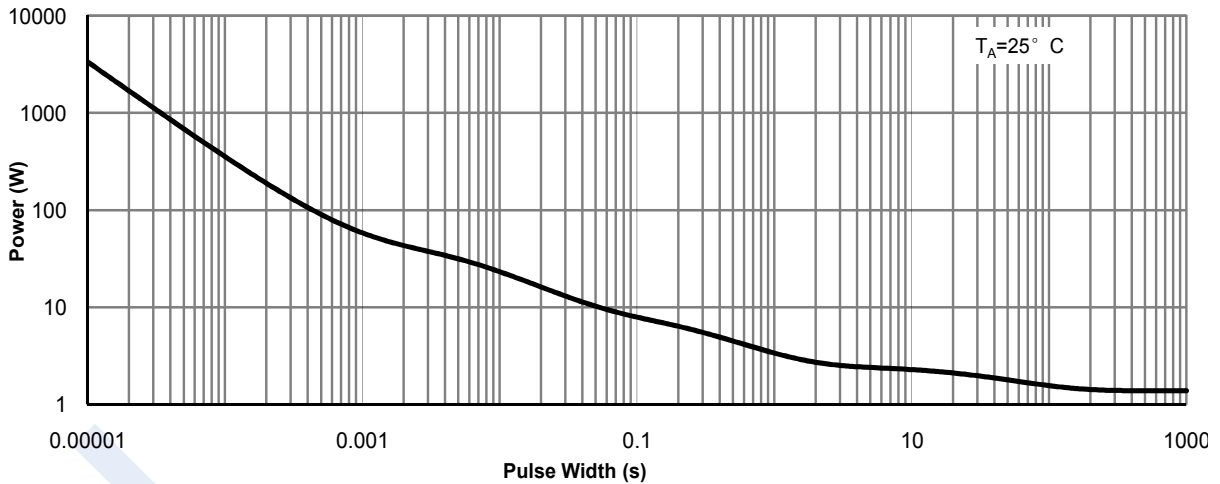


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

## Dual N-Channel MOSFET AO4892 (KO4892)

■ Typical Characteristics

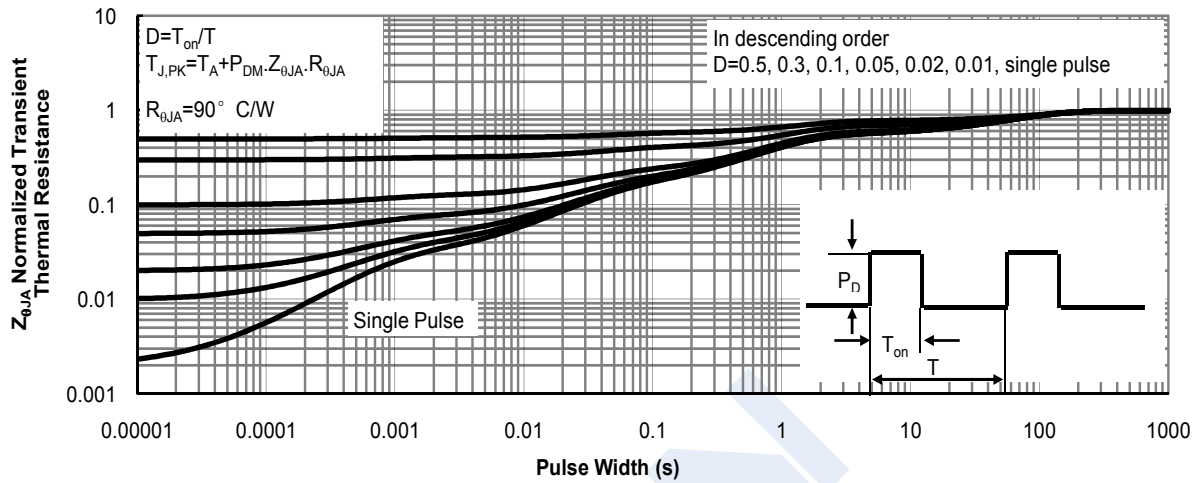


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)